

501.37476CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. OKOCHI, et al

Serial No.: Not yet assigned

Filing Date: August 4, 2003

For: SHARED MEMORY MULTIPROCESSOR SYSTEM

LETTER CLAIMING RIGHT OF PRIORITY

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 4, 2003

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, applicants hereby claim the right of priority based on:

Japanese Application No. 10-233832
Filed: August 20, 1998

A Certified copy of said application document was filed in parent application Serial No. 09/376,007, filed August 19, 1999, said certified copy filed in the United States Patent and Trademark Office on August 19, 1999. Acknowledgement thereof is respectfully requested.

Respectfully submitted,



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CIB/jdc
Enclosures
703/312-6600

日本国特許庁
PATENT OFFICE
JAPANESE GOVERNMENT

別紙添付の書類に記載されている事項は下記の出願書類に記載されて
る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed
in this Office.

出願年月日
Date of Application: 1998年 8月20日

願番号
Application Number: 平成10年特許願第233832号

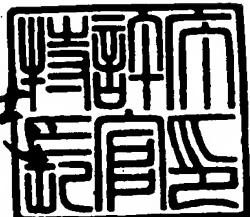
願人
Applicant(s): 株式会社日立製作所

1999年 3月19日

特許長官
Commissioner
Patent Office

明石 英也

1年佐山建志



出証番号 出証特平11-30157